

FIG. 1A

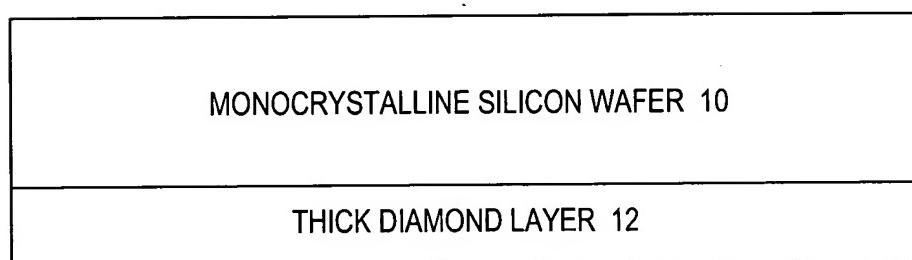


FIG. 1B

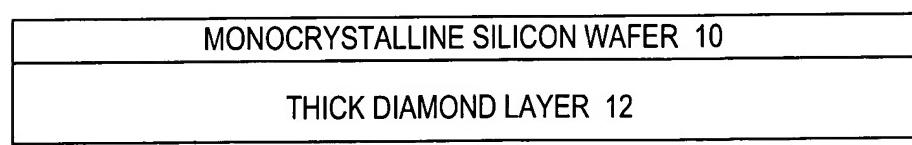


FIG. 1C

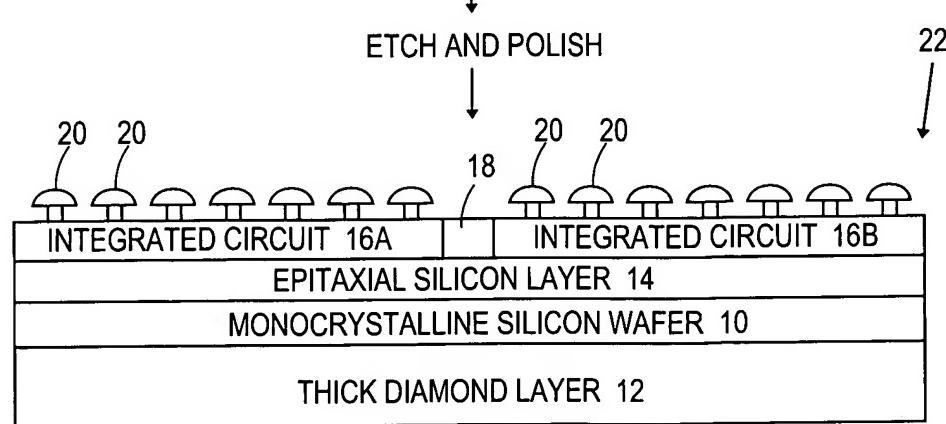
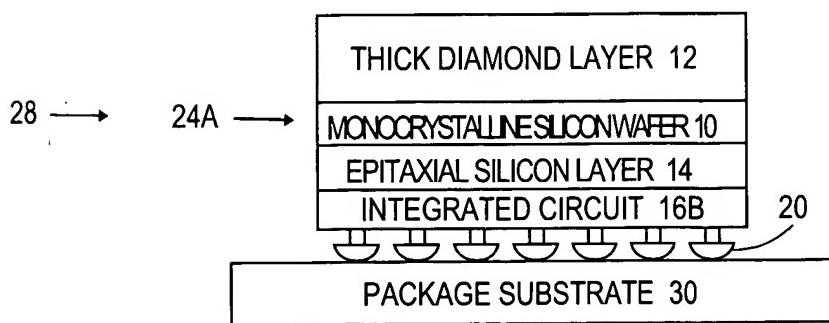
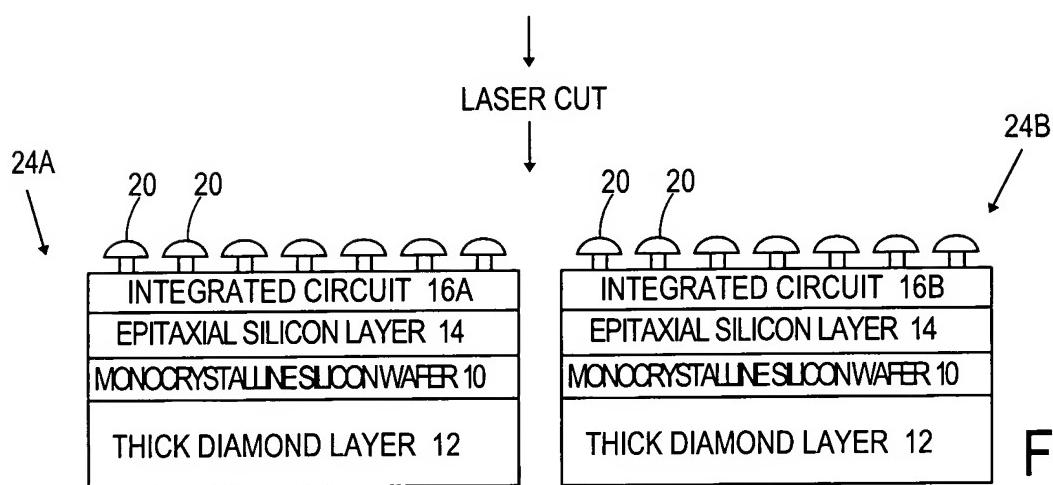
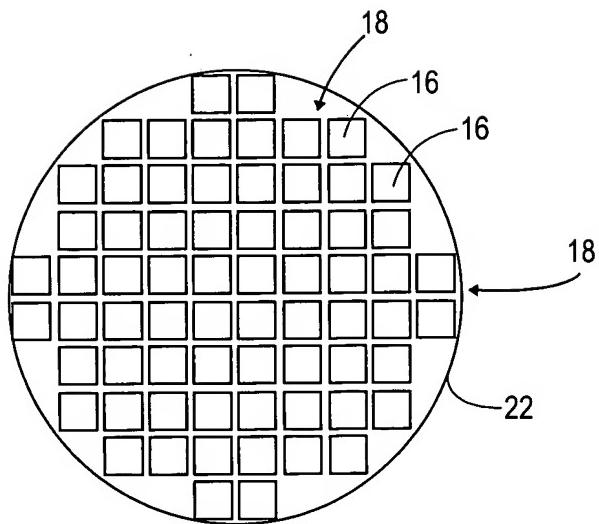


FIG. 1D



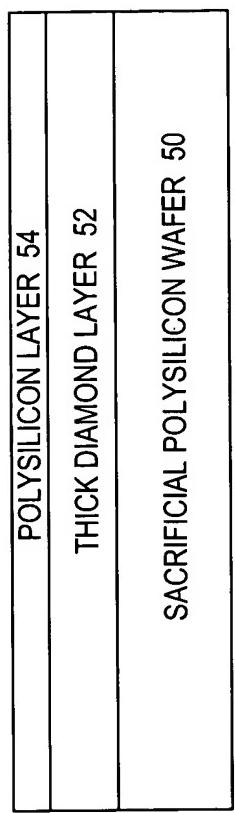


FIG. 2A

↓
FLIP
↓

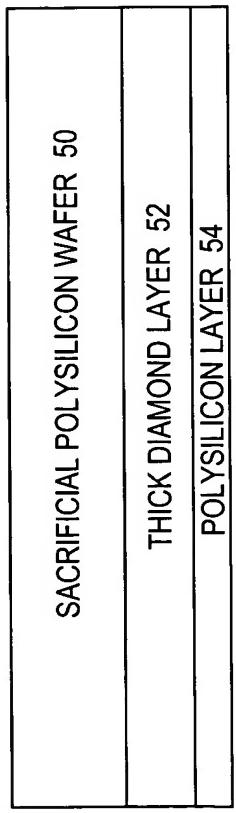


FIG. 2B

SILICON BOND
↓

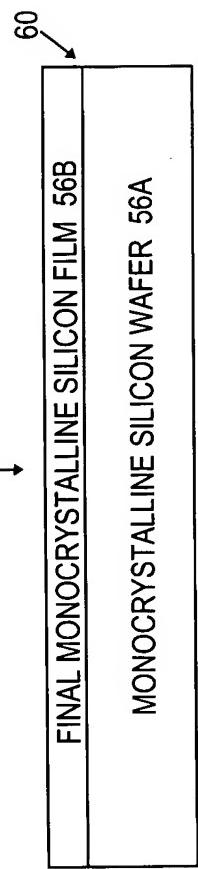
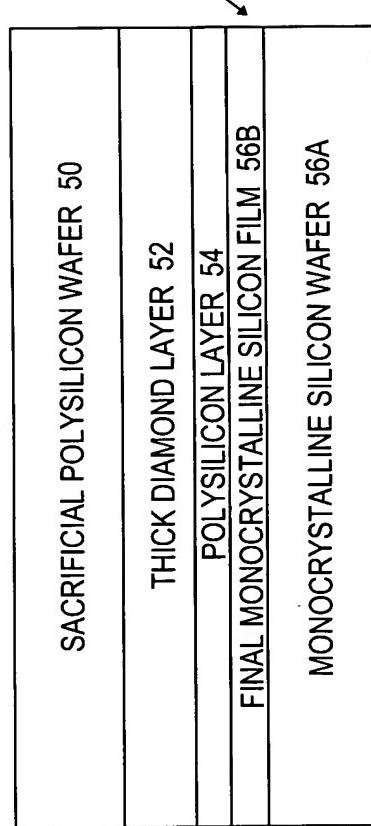


FIG. 2D

60
↓



FIG. 2E

↓ TO FIG. 2F

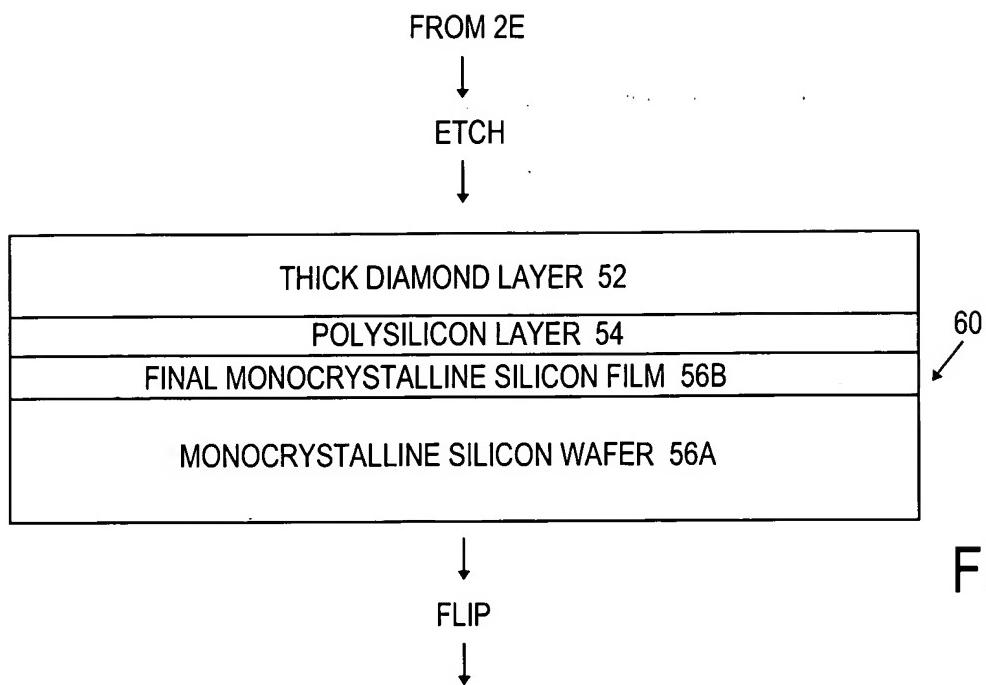


FIG. 2F

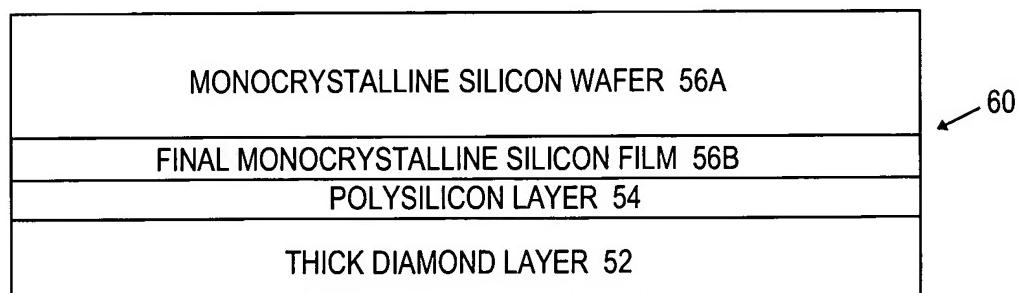
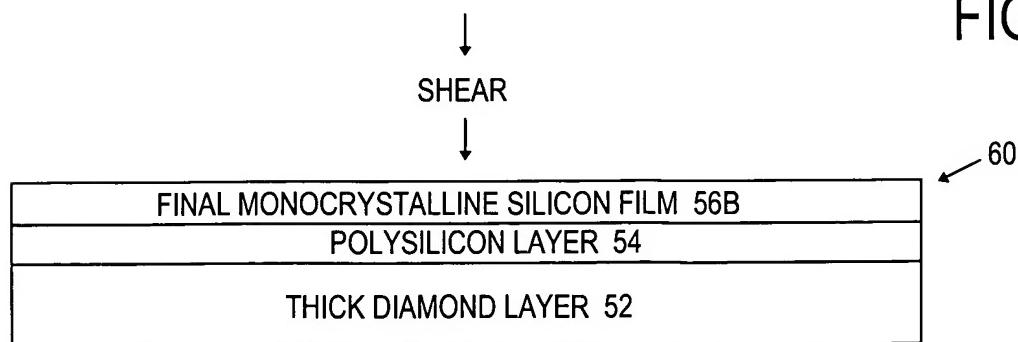


FIG. 2G



↓

ETCH AND POLISH

FIG. 2H

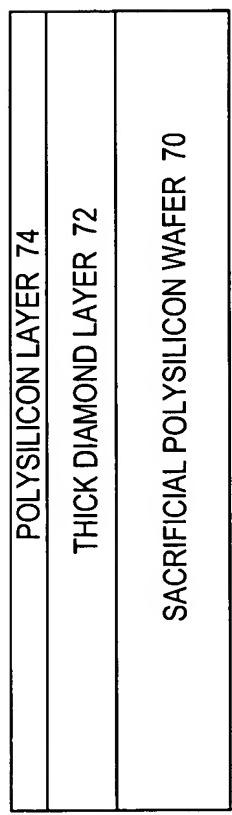


FIG. 3A

↓
FLIP
↓

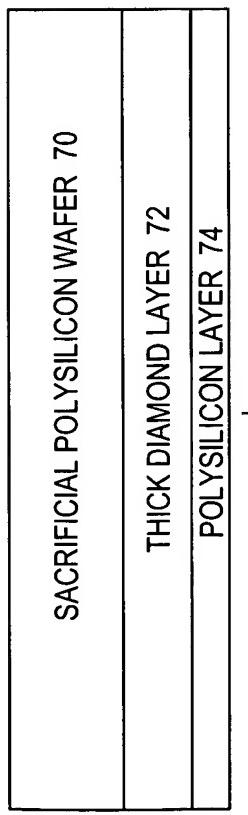


FIG. 3B

↓
SILICON BOND
↓

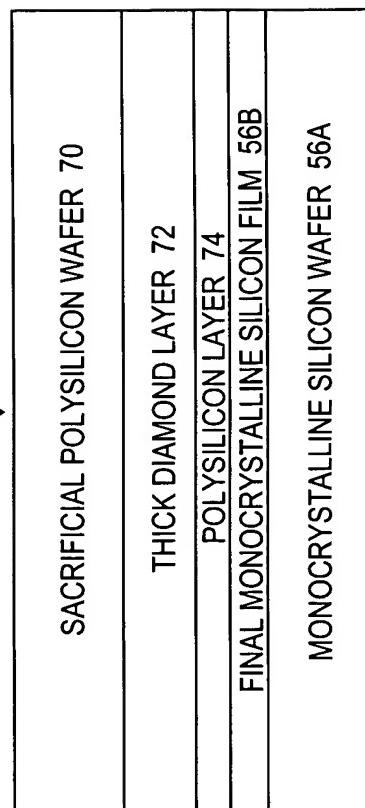


FIG. 3C

FIG. 3D

↓
⁸⁴



FIG. 3E

↓
TO FIG. 3F

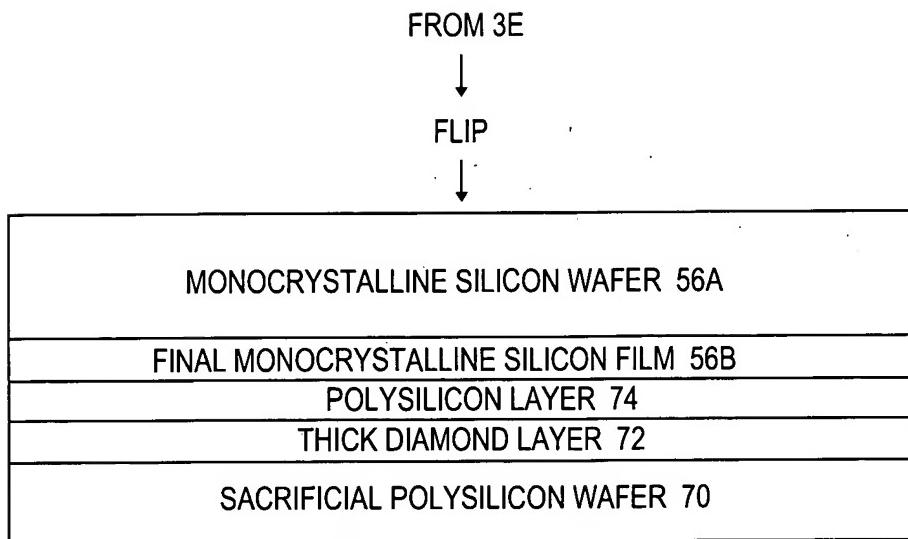


FIG. 3F

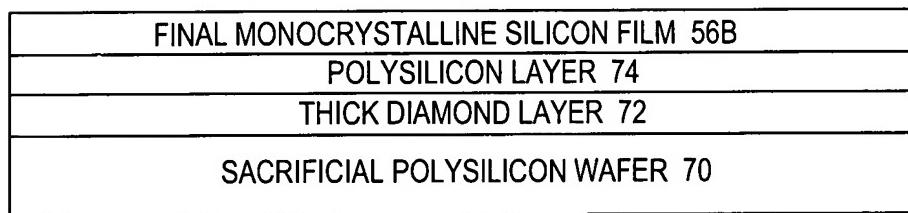


FIG. 3G

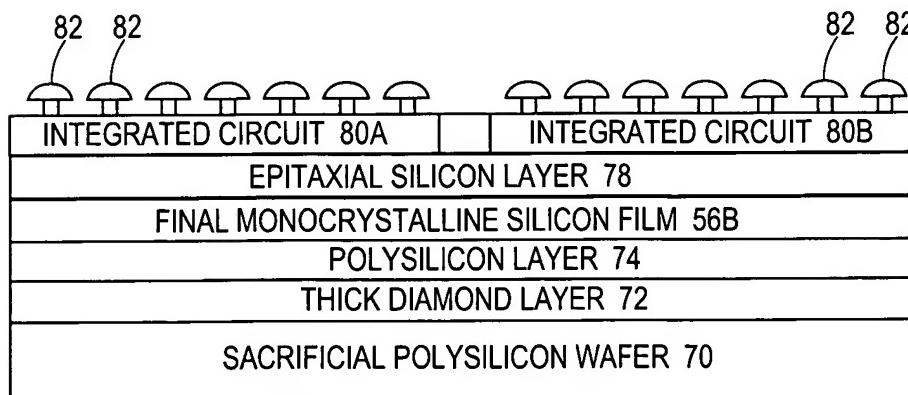


FIG. 3H

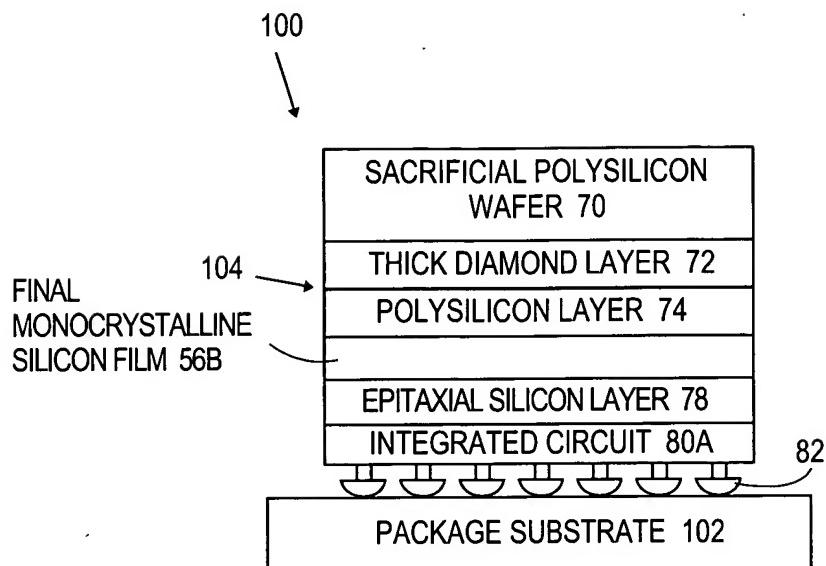


FIG. 3I

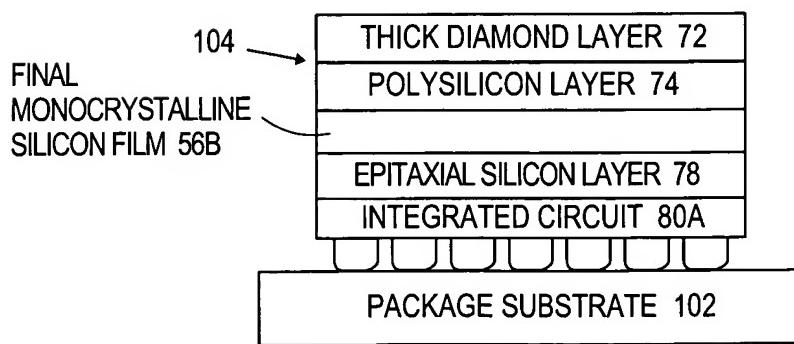


FIG. 3J